Constraint Languages for Parameterized Verification

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- The basic idea is to exhaustively search for bugs
- Particularly useful for verification of concurrent and reactive systems

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- and the corresponding safety requirements (e.g. nothing bad happens when the program runs)

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- A race condition occurs when the result of the computation depends on the order of execution
- They occur frequently in multitasking application (e.g. OS Kernel, multithreaded programs)
- They are dangerous: we must ensure consistency of shared data
- They are difficult to find and to reproduce:
 a different execution → a possible different instruction order
 → a possible different output

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 (Critical section problem, semaphores, etc).

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- Each process has a critical section in its code in which the shared resource is used
- Property to verify = mutual exclusion, i.e., in each execution at most one process is in the critical section

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- 3. Bounded Waiting: no starvation
- 4. Typical assumption Fairness: enabled instructions are eventually executed

Example: Lamport's Bakery Algorithm

```
begin integer j;
  L1: choosing[i] := 1;
       number[i] := 1 + maximum (number[1], ..., number[N]);
      choosing[i] := 0;
      for j = 1 step 1 until N do
         begin
           L2: if choosing[j] \neq 0 then goto L2;
           L3: if number[j] \neq 0 and (number[j], j) < (number[i],
                  i) then goto L3;
         end:
      critical section;
      number[i] := 0;
      noncritical section;
      goto L1;
end
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Automated Verification

 Automated verification methods like model checking can be applied to verify finite-state models of concurrent systems

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- Automated verification methods like model checking can be applied to verify finite-state models of concurrent systems
- To obtain a finite-state model: fix the number of processes, bound the domain of variable, use abstractions

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 - a property φ (called specification) that the final system is expected to satisfy.
- Output: yes if M satisfies φ , otherwise a counterexample
- The counterexample details why the model doesn't satisfy the specification.

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- It can be given as a transition system:

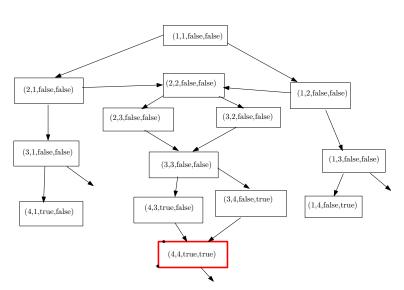
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- It can be given as a transition system:
 - A finite collection of states S
 - A transition relation $T \subseteq S \times S$ s.t. T(s, s') represents a transition from state s to state s'

Example

```
\begin{array}{lll} \mbox{Bool $wantP$, $wantQ$=} \mbox{false}; \\ \mbox{Proc P$=$} & \mbox{Proc Q$=$} \\ \mbox{1: Loop } \{ & \mbox{1: Loop } \{ \\ \mbox{2: $wait(!wantQ)$;} & \mbox{2: $wait(!wantP)$;} \\ \mbox{3: $wantP$ := true$;} & \mbox{3: $wantQ$ := true$;} \\ \mbox{4: Critical section$;} & \mbox{4: Critical section$;} \\ \mbox{5: $wantP$ := false$;} & \mbox{5: $wantQ$ := false$;} \\ \mbox{States} & = \{\langle 1, 1, false, false \rangle, \langle 2, 1, false, false \rangle, \ldots \} \\ \end{array}
```

Transition Relation



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 - ...
- In our example an assertion defined on a new variable critical == 1

Models with Infinite State-Space?

- Extended Finite-State Machines
 - Data: Unbounded local and global variables
 - Stack: Recursive Boolean programs
 - Channels: (Unreliable) Communication systems
 - . . .

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 - Families of systems

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- Parameterized Systems
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 - Families of systems
- Computability issues: What can be verified?

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- Model: a concurrent system with an arbitrary (finite) number of components
- Classes of topologies: Unordered, Linearly ordered, Tree-shaped, Graph-based
- Goal: Verify a Property for any number of processes (any topology in a given class)

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- Abstractions: Reductions to Finite-state Systems
- Regular Model Checking: Automata-based Representation of Sets of Configurations
- Constraint-based Model Checking: Constraints as Representation of Sets of Configurations

Example: Lamport's Bakery Algorithm for N-processes

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                   i) then goto L3:
         end:
       critical section;
       number[i] := 0;
       noncritical section:
       goto L1;
end
```

Verify mutual exclusion for any number of processes!

Plan of the Lessons

- Verification of Finite-state Systems
 - Computation Tree Logic
 - Model Checking Algorithms
- Verification of Infinite-state Systems
 - Constraint-based Model Checking
 - Parameterized Verification (Exact and Approximated Algorithms)
 - Unordered Systems
 - Linear Systems
 - Tree Topology
 - Graph Topology